



PTO/SB/01A (08-03)

Approved for use through 06/30/2006. OMB 0651-0032

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## DECLARATION (37 CFR 1.63) FOR UTILITY OR DESIGN APPLICATION USING AN APPLICATION DATA SHEET (37 CFR 1.76)

<b>Title of Invention</b>	<b>CLOCK LOGIC DOMINO CIRCUITS FOR HIGH-SPEED AND ENERGY EFFICIENT MICROPROCESSOR PIPELINES</b>
<p>As the below named inventor(s), I/we declare that:</p> <p>This declaration is directed to:</p> <p style="margin-left: 40px;"> <input type="checkbox"/> The attached application, or  <input checked="" type="checkbox"/> Application No. <u>10/730,002</u>, filed on <u>December 9, 2003</u>,  <input type="checkbox"/> as amended on _____ (if applicable);         </p> <p>I/we believe that I/we am/are the original and first inventor(s) of the subject matter which is claimed and for which a patent is sought;</p> <p>I/we have reviewed and understand the contents of the above-identified application, including the claims, as amended by any amendment specifically referred to above;</p> <p>I/we acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me/us to be material to patentability as defined in 37 CFR 1.56, including for continuation-in-part applications, material information which became available between the filing date of the prior application and the national or PCT International filing date of the continuation-in-part application.</p> <p>All statements made herein of my/own knowledge are true, all statements made herein on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001, and may jeopardize the validity of the application or any patent issuing thereon.</p>	

**FULL NAME OF INVENTOR(S)**Inventor one: Raymond Jit-Hung SUNGSignature: \_\_\_\_\_ Citizen of: CanadaInventor two: Duncan George ELLIOTTSignature: DJ Elliott May 4, 2004 Citizen of: Canada

Inventor three: \_\_\_\_\_

Signature: \_\_\_\_\_ Citizen of: \_\_\_\_\_

Inventor four: \_\_\_\_\_

Signature: \_\_\_\_\_ Citizen of: \_\_\_\_\_

☐ Additional inventors or a legal representative are being named on \_\_\_\_\_ additional form(s) attached hereto.

This collection of information is required by 35 U.S.C. 115 and 37 CFR 1.63. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1 minute to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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PTO/SB/81 (02-01)

Approved for use through 10/31/2002. OMB 0651-0035

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**POWER OF ATTORNEY OR  
AUTHORIZATION OF AGENT**

Application Number	
Filing Date	
First Named Inventor	Raymond Jit-Hung Sung
Title	CLOCK LOGIC DOMINO CIRCUITS FOR HIGH-SPEED AND ENERGY EFFICIENT MICROPROCESSOR PIPELINES
Group Art Unit	
Examiner Name	
Attorney Docket Number	9-16319-251US

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I am the:

☒ Applicant/Inventor.

☐ Assignee of record of the entire interest. See 37 CFR 3.71.  
Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96).
**SIGNATURE of Applicant or Assignee of Record**

Name Raymond Jit-Hung SUNG Duncan George ELLIOTT

Signature

Date

D. Elliott

May 9, 2004

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below\*.

☐ Total of \_\_\_\_\_ forms are submitted.

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